

**PROVISIONAL GRADE CARD**

Name : ANAND MANOHARRAO BHUJADE

Enrolment No. : MT17MVD001

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BC				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BC				
ENL513	IC DESIGN WITH HDL (DC)	4	BB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BC				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16	16	116	7.25	16	116		7.25					
DE 0	DC 16	HM 0	OC 0	0	DE 0	DC 16	HM 0	OC 0	0				
AU 0	ES 0	BS 0	Total	16	AU 0	ES 0	BS 0	Total	16				

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



## PROVISIONAL GRADE CARD

Name : ANSHUMAN SHUKLA

Enrolment No. : MT17MVD002

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	CC				
ENL513	IC DESIGN WITH HDL (DC)	4	BB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BB				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		124		7.75			16		124		7.75	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0						
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16						

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD**

Name : CHADALAWADA SRIHARI

Enrolment No. : MT17MVD003

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BC				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BC				
ENL513	IC DESIGN WITH HDL (DC)	4	AA				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BC				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			124			7.75				16			124			7.75		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD****Name : CHEDULURI GANESH****Enrolment No. : MT17MVD004****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BB				
ENL513	IC DESIGN WITH HDL (DC)	4	BC				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	AA				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			132			8.25				16			132			8.25		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



## PROVISIONAL GRADE CARD

Name : DARSHAN M  
Branch : V L S I DESIGN

Enrolment No. : MT17MVD005  
Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AA				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AA				
ENL513	IC DESIGN WITH HDL (DC)	4	AA				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BB				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			152			9.50				16			152			9.50		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD****Name : ISSARAPU CHAITHANYA SIVA TEJASWI****Enrolment No. : MT17MVD006****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AB				
ENL513	IC DESIGN WITH HDL (DC)	4	AB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BB				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		140		8.75			16		140		8.75	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0						
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16						

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Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD****Name : KAMSALA SREEHARI****Enrolment No. : MT17MVD007****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AA				
ENL513	IC DESIGN WITH HDL (DC)	4	BB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	AB				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			140			8.75				16			140			8.75		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**



## PROVISIONAL GRADE CARD

Name : KANDURI KARTHIK

Enrolment No. : MT17MVD008

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AB				
ENL513	IC DESIGN WITH HDL (DC)	4	AB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	AA				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		144		9.00			16		144		9.00	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0		
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16		

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SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : KETAN SISODIA

Enrolment No. : MT17MVD009

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	CD				
ENL513	IC DESIGN WITH HDL (DC)	4	AB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BB				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			120			7.50				16			120			7.50		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD**

Name : POOJA KAWDE

Enrolment No. : MT17MVD010

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BB				
ENL513	IC DESIGN WITH HDL (DC)	4	CC				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BC				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		116		7.25			16		116		7.25	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0		
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16		

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD**

Name : PUTLURU BHUVANESWARI

Enrolment No. : MT17MVD011

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AA				
ENL513	IC DESIGN WITH HDL (DC)	4	AB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BC				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		136		8.50			16		136		8.50	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0						
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16						

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD****Name : RAVEESH GOURISHETTY****Enrolment No. : MT17MVD012****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AA				
ENL513	IC DESIGN WITH HDL (DC)	4	AB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BB				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		144		9.00			16		144		9.00	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0						
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16						

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD****Name : RAZIA SULTANA SYED****Enrolment No. : MT17MVD013****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BB				
ENL513	IC DESIGN WITH HDL (DC)	4	BB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	AB				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		136		8.50			16		136		8.50	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0		
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16		

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**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD****Name : UTKARSH RAJESH INGLE****Enrolment No. : MT17MVD015****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BC				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BC				
ENL513	IC DESIGN WITH HDL (DC)	4	BB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BC				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			116			7.25				16			116			7.25		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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**Asst. Registrar (Examination)**

**PROVISIONAL GRADE CARD**

Name : VAIDHYANADHAN DEEPAK

Enrolment No. : MT17MVD016

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	BB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AB				
ENL513	IC DESIGN WITH HDL (DC)	4	BB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	AB				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		136		8.50			16		136		8.50	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0						
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16						

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**



## PROVISIONAL GRADE CARD

Name : VASANTADA DHARMA RAJU

Enrolment No. : MT17MVD017

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	BB				
ENL513	IC DESIGN WITH HDL (DC)	4	AB				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	BC				

SGPA	Credit			EGP			SGPA			CGPA	Credit			EGP			CGPA		
	16			132			8.25				16			132			8.25		
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD****Name : VEERAVILLI KRISHNA GEETHIKA****Enrolment No. : MT17MVD018****Branch : V L S I DESIGN****Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2017</b>							
ENL509	ADVANCED DEVICE PHYSICS (DC)	4	AB				
ENL511	CMOS DIGITAL VLSI DESIGN (DC)	4	AA				
ENL513	IC DESIGN WITH HDL (DC)	4	AA				
ENL517	MICRO AND NANO FABRICATION TECHNOLOGIES (DC)	4	AA				

SGPA	Credit		EGP		SGPA		CGPA	Credit		EGP		CGPA	
	16		156		9.75			16		156		9.75	
DE 0	DC 16	HM 0	OC 0	DE 0	DC 16	HM 0	OC 0						
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16						

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Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : ABHIMANNIU RAVEENDRAN**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD001**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AB	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	BB
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AB
ENL433	DIGITAL IC DESIGN (DC)	3	AB	ENL503	ANALOG IC DESIGN (DC)	3	AA
ENP431	DEVICE SIMULATION (DC)	1	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL505	NANO ELECTRONICS (DC)	3	AA
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AA
ENP510	VLSI TECHNOLOGY (DE)	1	AA	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	AB	ENP505	NANO ELECTRONICS (DC)	1	AA
<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>CGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>16</b>	<b>148</b>	<b>9.25</b>		<b>16</b>	<b>148</b>	<b>9.25</b>
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AA
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AA
<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>
	<b>9</b>	<b>84</b>	<b>9.33</b>
DE 6	DC 3	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	<b>18</b>	<b>171</b>	<b>9.50</b>		<b>34</b>	<b>319</b>	<b>9.38</b>
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : AMIT JARIKA  
Branch : V L S I DESIGN

Enrolment No. : MT16MVD002  
Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	CC	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	CC	ENL503	ANALOG IC DESIGN (DC)	3	CD
ENL433	DIGITAL IC DESIGN (DC)	3	CC	ENL504	VLSI SYSTEM DESIGN (DC)	3	BB
ENP431	DEVICE SIMULATION (DC)	1	BC	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	BB	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	BC
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BB
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	CC	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	104	6.50		16	104	6.50
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	135	7.50		34	239	7.03
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	BC
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BC
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	BB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	66	7.33		43	305	7.09
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : AMMU LAKSHMY R

Enrolment No. : MT16MVD003

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AA	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	AB
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AA
ENL433	DIGITAL IC DESIGN (DC)	3	AA	ENL503	ANALOG IC DESIGN (DC)	3	AA
ENP431	DEVICE SIMULATION (DC)	1	AA	ENL504	VLSI SYSTEM DESIGN (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL505	NANO ELECTRONICS (DC)	3	AA
ENP433	DIGITAL IC DESIGN (DC)	1	AA	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AA
ENP510	VLSI TECHNOLOGY (DE)	1	AA	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	AA	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	160	10.00		16	160	10.00
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	177	9.83		34	337	9.91
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AA
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AA
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	90	10.00		43	427	9.93
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : B LAKSHMI VENKATA SURYA PRAKASH**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD005**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	CC	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	CC
ENL432	ADVANCED VLSI DESIGN (DC)	3	BC	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BC
ENL433	DIGITAL IC DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	CC
ENP431	DEVICE SIMULATION (DC)	1	BC	ENL504	VLSI SYSTEM DESIGN (DC)	3	BB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL505	NANO ELECTRONICS (DC)	3	BC
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BC
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	BC	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	114	7.13		16	114	7.13
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	127	7.06		34	241	7.09
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BC
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	BC

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	69	7.67		43	310	7.21
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : LONE BALAJI SHIVRAM**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD006**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BC	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	BC
ENL433	DIGITAL IC DESIGN (DC)	3	CC	ENL504	VLSI SYSTEM DESIGN (DC)	3	BB
ENP431	DEVICE SIMULATION (DC)	1	BC	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	AB
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	BB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	CC	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	110	6.88		16	110	6.88
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	147	8.17		34	257	7.56
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	78	8.67		43	335	7.79
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : CHINTA CHARITHA

Enrolment No. : MT16MVD007

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BB	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	CC
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AB
ENL433	DIGITAL IC DESIGN (DC)	3	AA	ENL503	ANALOG IC DESIGN (DC)	3	BB
ENP431	DEVICE SIMULATION (DC)	1	AB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL505	NANO ELECTRONICS (DC)	3	BC
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	AA	ENP505	NANO ELECTRONICS (DC)	1	AA
<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>16</b>	<b>151</b>	<b>9.44</b>		<b>18</b>	<b>146</b>	<b>8.11</b>
DE 1	DC 15	HM 0	OC 0	DE 7	DC 11	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 18
<b>CGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>	<b>CGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>16</b>	<b>151</b>	<b>9.44</b>		<b>34</b>	<b>297</b>	<b>8.74</b>
DE 1	DC 15	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	BB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AA
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AB

<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>CGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>9</b>	<b>81</b>	<b>9.00</b>		<b>43</b>	<b>378</b>	<b>8.79</b>
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : DHULE SAGAR BHIMRAO**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD008**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BC	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL432	ADVANCED VLSI DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	CC
ENL433	DIGITAL IC DESIGN (DC)	3	CC	ENL504	VLSI SYSTEM DESIGN (DC)	3	CC
ENP431	DEVICE SIMULATION (DC)	1	CC	ENL505	NANO ELECTRONICS (DC)	3	BB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	BC
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AA
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	CC	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	111	6.94		16	111	6.94
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	133	7.39		34	244	7.18
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	BB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	BC

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	69	7.67		43	313	7.28
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**  
 Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**





**PROVISIONAL GRADE CARD**

**Name : JAGRITI SRIVASTAVA**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD009**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AA
ENL432	ADVANCED VLSI DESIGN (DC)	3	AB	ENL503	ANALOG IC DESIGN (DC)	3	AB
ENL433	DIGITAL IC DESIGN (DC)	3	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AA
ENP431	DEVICE SIMULATION (DC)	1	BC	ENL505	NANO ELECTRONICS (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	BB
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	BC	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	131	8.19		16	131	8.19
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	170	9.44		34	301	8.85
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AB
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	81	9.00		43	382	8.88
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : KALAKONDA SNEHAMAYI**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD010**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BC	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BC
ENL433	DIGITAL IC DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	AA
ENP431	DEVICE SIMULATION (DC)	1	AB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	BB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	AA	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	137	8.56		16	137	8.56
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	156	8.67		34	293	8.62
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AB
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	84	9.33		43	377	8.77
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : BHAGAT KHEMNAND BANDUJI**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD011**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	CC	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	CC
ENL433	DIGITAL IC DESIGN (DC)	3	CC	ENL504	VLSI SYSTEM DESIGN (DC)	3	BC
ENP431	DEVICE SIMULATION (DC)	1	CC	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	BB	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	BC
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BB
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	CC	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	106	6.63		16	106	6.63
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	134	7.44		34	240	7.06
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	BB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	BB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	72	8.00		43	312	7.26
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : KHUUSHI  
Branch : V L S I DESIGN

Enrolment No. : MT16MVD012  
Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BC	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	DD
ENL432	ADVANCED VLSI DESIGN (DC)	3	AB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL433	DIGITAL IC DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	AB
ENP431	DEVICE SIMULATION (DC)	1	CC	ENL504	VLSI SYSTEM DESIGN (DC)	3	BB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL505	NANO ELECTRONICS (DC)	3	BB
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	BB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	BC	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	121	7.56		16	121	7.56
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	139	7.72		34	260	7.65
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AA
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	81	9.00		43	341	7.93
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : KOKKULA VINEYSARATHI

Enrolment No. : MT16MVD013

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AA	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	BB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL433	DIGITAL IC DESIGN (DC)	3	AB	ENL503	ANALOG IC DESIGN (DC)	3	AB
ENP431	DEVICE SIMULATION (DC)	1	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL505	NANO ELECTRONICS (DC)	3	BB
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BB
ENP510	VLSI TECHNOLOGY (DE)	1	BB	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	BC	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	136	8.50		16	136	8.50
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	151	8.39		34	287	8.44
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AB
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	81	9.00		43	368	8.56
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : KUPPILI M VENKATA VARAHA RAMKIRAN**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD014**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BC	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	CC
ENL432	ADVANCED VLSI DESIGN (DC)	3	AB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL433	DIGITAL IC DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	AB
ENP431	DEVICE SIMULATION (DC)	1	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BB
ENP510	VLSI TECHNOLOGY (DE)	1	BB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	BC	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	123	7.69		16	123	7.69
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	149	8.28		34	272	8.00
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	BB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BC
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	BB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	69	7.67		43	341	7.93
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**  
 Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : DESHPANDE LAUKIK DEEPAK**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD015**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AA	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	AB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AB
ENL433	DIGITAL IC DESIGN (DC)	3	BB	ENL503	ANALOG IC DESIGN (DC)	3	AA
ENP431	DEVICE SIMULATION (DC)	1	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AA
ENP510	VLSI TECHNOLOGY (DE)	1	BB	ENP503	ANALOG IC DESIGN (DC)	1	AB
PHL401	VLSI TECHNOLOGY (DC)	3	AA	ENP505	NANO ELECTRONICS (DC)	1	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	146	9.13		16	146	9.13
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	163	9.06		34	309	9.09
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AA
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	BB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	81	9.00		43	390	9.07
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : MILIND KRUSHNARAO GIRADKAR**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD016**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL432	ADVANCED VLSI DESIGN (DC)	3	AB	ENL503	ANALOG IC DESIGN (DC)	3	AB
ENL433	DIGITAL IC DESIGN (DC)	3	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AB
ENP431	DEVICE SIMULATION (DC)	1	BB	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	AA
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BB
ENP510	VLSI TECHNOLOGY (DE)	1	AA	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	BB	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	138	8.63		16	138	8.63
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	163	9.06		34	301	8.85
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	BB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	78	8.67		43	379	8.81
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**





**PROVISIONAL GRADE CARD**

**Name : KADAM SANKET VILASRAO**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD019**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BB	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AB
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL503	ANALOG IC DESIGN (DC)	3	AA
ENL433	DIGITAL IC DESIGN (DC)	3	AB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AB
ENP431	DEVICE SIMULATION (DC)	1	AB	ENL505	NANO ELECTRONICS (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL506	COMPUTER ORGANIZATION AND OPERATING SYSTEM (DE)	3	AA
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	AB	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	AB	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	145	9.06		16	145	9.06
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	173	9.61		34	318	9.35
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AA
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	87	9.67		43	405	9.42
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



## PROVISIONAL GRADE CARD

Name : SUYOG TRYAMBAK INGLE

Enrolment No. : MT16MVD020

Branch : V L S I DESIGN

Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	BC	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	CD
ENL432	ADVANCED VLSI DESIGN (DC)	3	BC	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL433	DIGITAL IC DESIGN (DC)	3	BC	ENL503	ANALOG IC DESIGN (DC)	3	BC
ENP431	DEVICE SIMULATION (DC)	1	BC	ENL504	VLSI SYSTEM DESIGN (DC)	3	BB
ENP432	ADVANCED VLSI DESIGN (DC)	1	BB	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP433	DIGITAL IC DESIGN (DC)	1	BB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	BC	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	CC	ENP505	NANO ELECTRONICS (DC)	1	AB
<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>16</b>	<b>111</b>	<b>6.94</b>		<b>18</b>	<b>139</b>	<b>7.72</b>
DE 1	DC 15	HM 0	OC 0	DE 7	DC 11	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 18
<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>CGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>9</b>	<b>78</b>	<b>8.67</b>		<b>43</b>	<b>328</b>	<b>7.63</b>
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

### AUTUMN (July-Nov)2017

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	BB
ENL523	DISPLAY MATERIALS AND TECHNOLOGIES (DE)	3	AB

<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>CGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>9</b>	<b>78</b>	<b>8.67</b>		<b>43</b>	<b>328</b>	<b>7.63</b>
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points

SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course

( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

Name : VIJITHA V J  
Branch : V L S I DESIGN

Enrolment No. : MT16MVD021  
Degree : Master of Technology

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AA	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	CC
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	BB
ENL433	DIGITAL IC DESIGN (DC)	3	AB	ENL503	ANALOG IC DESIGN (DC)	3	BB
ENP431	DEVICE SIMULATION (DC)	1	BB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AB
ENP432	ADVANCED VLSI DESIGN (DC)	1	AB	ENL505	NANO ELECTRONICS (DC)	3	BC
ENP433	DIGITAL IC DESIGN (DC)	1	AB	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	BB
ENP510	VLSI TECHNOLOGY (DE)	1	AA	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	AB	ENP505	NANO ELECTRONICS (DC)	1	AB
<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>SGPA</b>	<b>SGPA</b>	<b>Credit</b>	<b>EGP</b>	<b>CGPA</b>
	<b>16</b>	<b>150</b>	<b>9.38</b>		<b>18</b>	<b>141</b>	<b>7.83</b>
DE 1	DC 15	HM 0	OC 0	DE 7	DC 11	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 18

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	<b>9</b>	<b>75</b>	<b>8.33</b>		<b>43</b>	<b>366</b>	<b>8.51</b>
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	<b>34</b>	<b>291</b>	<b>8.56</b>		<b>34</b>	<b>291</b>	<b>8.56</b>
DE 8	DC 26	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 34	AU 0	ES 0	BS 0	Total 34

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**



**PROVISIONAL GRADE CARD**

**Name : VISHNUBHOTLA VAISHNAVI**  
**Branch : V L S I DESIGN**

**Enrolment No. : MT16MVD022**  
**Degree : Master of Technology**

Course	Title	Cr	Gr	Course	Title	Cr	Gr
<b>AUTUMN (July-Nov)2016</b>				<b>SPRING (Jan-May) 2017</b>			
ENL431	PHYSICAL ELECTRONICS (DC)	3	AB	ECL409	RADIO FREQUENCY CIRCUIT DESIGN (DE)	3	BC
ENL432	ADVANCED VLSI DESIGN (DC)	3	AA	ENL435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	3	AA
ENL433	DIGITAL IC DESIGN (DC)	3	AA	ENL503	ANALOG IC DESIGN (DC)	3	AA
ENP431	DEVICE SIMULATION (DC)	1	AB	ENL504	VLSI SYSTEM DESIGN (DC)	3	AA
ENP432	ADVANCED VLSI DESIGN (DC)	1	AA	ENL505	NANO ELECTRONICS (DC)	3	AB
ENP433	DIGITAL IC DESIGN (DC)	1	AA	ENP435	MICRO ELECTRO MECHANICAL SYSTEMS (DE)	1	AB
ENP510	VLSI TECHNOLOGY (DE)	1	BC	ENP503	ANALOG IC DESIGN (DC)	1	AA
PHL401	VLSI TECHNOLOGY (DC)	3	AA	ENP505	NANO ELECTRONICS (DC)	1	AA

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	16	153	9.56		16	153	9.56
DE 1	DC 15	HM 0	OC 0	DE 1	DC 15	HM 0	OC 0
AU 0	ES 0	BS 0	Total 16	AU 0	ES 0	BS 0	Total 16

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	18	167	9.28		34	320	9.41
DE 7	DC 11	HM 0	OC 0	DE 8	DC 26	HM 0	OC 0
AU 0	ES 0	BS 0	Total 18	AU 0	ES 0	BS 0	Total 34

**AUTUMN (July-Nov)2017**

END501	PROJECT PHASE - I (DC)	3	AB
ENL507	DESIGN FOR TESTABILITY YIELD AND RELIABILITY (DE)	3	AA
ENL521	REAL TIME SIGNAL PROCESSING (DE)	3	AB

SGPA	Credit	EGP	SGPA	CGPA	Credit	EGP	CGPA
	9	84	9.33		43	404	9.40
DE 6	DC 3	HM 0	OC 0	DE 14	DC 29	HM 0	OC 0
AU 0	ES 0	BS 0	Total 9	AU 0	ES 0	BS 0	Total 43

**Note : This grade card is exclusively for internal use**

Abbreviations : Cr - Credits, Gr - Grade, AU - Audit, SPI - Semester Performance Index, CPI - Cumulative Performance Index, EGP - Earned Grade Points  
 SGPA - Semester Grade Point Average, CGPA - Cumulative Grade Point Average, W - repeat the Course  
 ( This Statement is subject to correction, if any )

Date : 13-Dec-2017

**Asst. Registrar (Examination)**